

## CLAIMS

We claim:

- 5           1.     A display device, comprising:  
              an image formation apparatus; and  
              a controller, operably connected to the image formation apparatus,  
configured to perform a power down process after a predetermined delay period  
in response to a receipt of a first power command, and to not perform the power  
10   down process in response to a receipt of a second power command prior to the  
end of the predetermined delay period.
2.     A display device as claimed in claim 1, wherein the image  
formation apparatus includes a light source.
- 15           3.     A display device as claimed in claim 2, further comprising:  
              a lens adapted to focus light from the light source onto a projection  
surface in spaced relation to the display device.
- 20           4.     A display device as claimed in claim 1, wherein the predetermined  
delay period is at least two seconds.
5.     A display device as claimed 1, wherein  
              the image formation apparatus creates a plurality of pixels on a  
25   display surface; and  
              the controller is configured to control the image formation  
apparatus such that the pixels are in an OFF state during the predetermined  
delay period.
- 30           6.     A display device as claimed 5, wherein the controller is configured  
to control the image formation apparatus such that the pixels are returned to an

ON state in response to the receipt of a second power command prior to the end of the predetermined delay period.

5        7.     A display device as claimed in claim 1, wherein the image formation apparatus includes a plurality of mirrors.

10        8.     A display device as claimed in claim 1, further comprising:  
             a power button that generates the first and second power commands.

             9.     A display device as claimed in claim 8, wherein the power button is associated with a remote control.

15        10.    A display device, comprising:  
             an image formation apparatus, adapted to produce a plurality of pixels having an ON state and an OFF state, including a light source having and ON state and an OFF state; and  
             a controller, operably connected to the image formation apparatus, configured to (1) switch the light source to the ON state in response to a first  
20        power command, (2) switch at least a substantial majority of the pixels to the OFF state and to maintain the light source in the ON state in response to a second power command, and (3) switch the light source to the OFF state in response to a failure of a third power command to be received within a predetermined delay period after second power command.

25        11.    A display device as claimed in claim 10, further comprising:  
             a lens adapted to focus light from the light source onto a projection surface in spaced relation to the display device.

30        12.    A display device as claimed in claim 10, wherein the predetermined delay period is at least two seconds.

13. A display device as claimed in claim 10, wherein the controller is configured to switch all of the pixels to the OFF state in response to the second power command.

5           14. A display device as claimed in claim 10, wherein the controller is configured to switch the pixels to the ON state and to maintain the light source in the ON state in response to a receipt of a third power command within the predetermined delay period after second power command.

10           15. A display device as claimed in claim 10, wherein the image forming apparatus includes a plurality of mirrors.

          16. A display device, comprising:  
              means for supplying light having an ON state and an OFF state;  
15            means for creating a plurality of pixels having an ON state and an OFF state with the light;  
              means for switching at least a substantial majority of the pixels to the OFF state, while maintaining the means for supplying light in the ON state, in response to a first power command; and  
20            means for switching the means for supplying light to the OFF state in response to a failure of a second power command to be received within a predetermined delay period after first power command.

          17. A display device as claimed in claim 16, wherein the means for  
25           switching comprises means for switching all of the pixels to the OFF state, while maintaining the means for supplying light in the ON state, in response to a first power command.

          18. A display device, comprising:  
30            means for forming images; and  
              means, operably connected to the means for forming images, for performing a power down process after a predetermined delay period in

response to a receipt of a first power command, and not performing the power down process in response to a receipt of a second power command prior to the end of the predetermined delay period.

- 5           19.    A method of operating a display device, comprising the steps of:  
                  switching a light source to an ON state in response to a first power  
                  command;  
                  directing light from the light source onto a display surface;  
                  preventing the light from the light source from being directed onto  
10   the display surface, while maintaining the light source in the ON state, in  
                  response to a second power command; and  
                  switching the light source to an OFF state in response to a failure  
                  of a third power command to be received within a predetermined delay period  
                  after second power command.
- 15           20.    A method as claimed in claim 19, further comprising the step of:  
                  maintaining the light source in the ON state in response to the  
                  receipt of the third power command within the predetermined delay period after  
                  second power command.
- 20           21.    A method as claimed in claim 20, further comprising the step of:  
                  directing light from the light source onto the display surface in  
                  response to the receipt of the third power command within the predetermined  
                  delay period after second power command.
- 25           22.    A method as claimed in claim 19, wherein the predetermined delay  
                  period is at least two seconds.
- 30           23.    A method as claimed in claim 19, wherein  
                  the step of directing light from the light source onto a display  
                  surface comprises reflecting light from the light source onto a display surface by  
                  turning a plurality of mirrors toward the light source; and

the step of preventing the light from the light source from being directed onto the display surface comprises turning the plurality of mirrors away from the light source, while maintaining the light source to an ON state, in response to a second power command.

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24. A method as claimed in claim 19, wherein the step of projecting light comprises projecting an image onto a screen.

25. A method of operating a display device, comprising the steps of:  
10 performing a power down process after a predetermined delay period in response to a receipt of a first power command; and  
not performing the power down process in response to a receipt of a second power command prior to the end of the predetermined delay period.

15 26. A method as claimed in claim 25, further comprising the step of:  
switching a plurality of pixels to an OFF state in response to the first power command.

20 27. A method as claimed in claim 26, further comprising the step of:  
switching the plurality of pixels to an ON state in response to the receipt of the second power command prior to the end of the predetermined delay period.

25 28. A method as claimed in claim 25, wherein the predetermined delay period is at least two seconds.

29. A method as claimed in claim 25, wherein the step of performing a power down process includes the step of turning a light source to an OFF state.

30 30. Computer memory encoded with executable instructions, the instructions comprising steps for:

performing a power down process after a predetermined delay period in response to a receipt of a first power command; and

not performing the power down process in response to a receipt of a second power command prior to the end of the predetermined delay period.

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31. Computer memory as claimed in claim 30, wherein the display device includes a light source and the step of performing a power down process includes placing the light source in an OFF state.

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32. Computer memory as claimed in claim 31, wherein the display device includes a light source and the step of performing a power down process includes maintaining the light source in an ON state during the predetermined delay period.

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33. Computer memory as claimed in claim 32, wherein the step of not performing the power down process includes maintaining the light source in the ON state after the end of the predetermined delay period.

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34. Computer memory as claimed in claim 30, wherein the predetermined delay period is at least two seconds.

35. Computer memory as claimed in claim 30, wherein the display device creates a plurality of pixels on a display surface, the instructions further comprising steps for:

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placing the pixels in an OFF state during the predetermined delay period.

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36. Computer memory as claimed in claim 30, wherein the first and second power commands comprise signals from a power button.

37. Computer memory encoded with executable instructions for operating an image formation apparatus, the image formation apparatus being

adapted to produce a plurality of pixels having an ON state and an OFF state and including a light source having an ON state and an OFF state, the instructions comprising steps for:

5       switching the light source to an ON state in response to a first power command;

          switching at least a substantial majority of the pixels to the OFF state and for maintaining the light source in the ON state in response to a second power command; and

10       switching the light source to the OFF state in response to a failure of a third power command to be received within a predetermined delay period after second power command.

38.   Computer memory as claimed in claim 37, wherein the predetermined delay period is at least two seconds.

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39.   Computer memory as claimed in claim 37, wherein the step of switching at least a substantial majority of the pixels to the OFF state switch all of the pixels to the OFF state.

20       40.   Computer memory as claimed in claim 37, the instructions further comprising steps for:

          switching the pixels to an ON state and maintaining the light source in an ON state in response to a receipt of a third power command within the predetermined delay period after second power command.